MYD-CZU3EG/4EV/5EV-V2 PIN MAP

Version：V1.0　2023/02/02

Function	SoC Pin Name	Bank	Ball	Net Name	J2		Net Name	Ball	Bank	SoC Pin Name	Function
Ground				GND	1	2	VCC_IN_COM			DC 3.3V	SOM POWER DC 3.3V
sfp	MGTHTXP3_224	Bank 224	N4	MGTHTXP3_224	3	4	VCC_IN_COM			DC 3.3V	SOM POWER DC 3.3V
sfp	MGTHTXN3_224	Bank 224	N3	MGTHTXN3_224	5	6	VCC_IN_COM			DC 3.3V	SOM POWER DC 3.3V
Ground				GND	7	8	GND			Ground	Ground
sfp	MGTHRXP3_224	Bank 224	P2	MGTHRXP3_224	9	10	MGTHTXP2_224	R4	Bank 224	MGTHTXP2_224	sfp
sfp	MGTHRXN3_224	Bank 224	P1	MGTHRXN3_224	11	12	MGTHTXN2_224	R3	Bank 224	MGTHTXN2_224	sfp
Ground				GND	13	14	GND			Ground	Ground
sfp	MGTHTXP1_224	Bank 224	U4	MGTHTXP1_224	15	16	MGTHRXP2_224	T2	Bank 224	MGTHRXP2_224	sfp
sfp	MGTHTXN1_224	Bank 224	U3	MGTHTXN1_224	17	18	MGTHRXN2_224	T1	Bank 224	MGTHRXN2_224	sfp
Ground				GND	19	20	GND			Ground	Ground
sfp	MGTHRXP1_224	Bank 224	V2	MGTHRXP1_224	21	22	MGTHTXP0_224	W4	Bank 224	MGTHTXP0_224	sfp
sfp	MGTHRXN1_224	Bank 224	V1	MGTHRXN1_224	23	24	MGTHTXN0_224	W3	Bank 224	MGTHTXN0_224	sfp
Ground				GND	25	26	GND			Ground	Ground
sfp	MGTREFCLK0P_224	Bank 224	Y6	MGTREFCLK0P_224	27	28	MGTHRXP0_224	Y2	Bank 224	MGTHRXP0_224	sfp
sfp	MGTREFCLK0N_224	Bank 224	Y5	MGTREFCLK0N_224	29	30	MGTHRXN0_224	Y1	Bank 224	MGTHRXN0_224	sfp
Ground				GND	31	32	GND			Ground	Ground
IO_T0U_N12_VRP_65	IO_T0U_N12_VRP_65	Bank 65	W9	IO_T0U_N12_VRP_65	33	34	IO_L4P_T0U_N6_DBC_AD7P_SMBALERT_65	R8	Bank 65	IO_L4P_T0U_N6_DBC_AD7P_SMBALERT_65	FMC LA32_P
SFP_RB_TX_Disable	IO_T2U_N12_65	Bank 65	P9	IO_T2U_N12_65	35	36	IO_L4N_T0U_N7_DBC_AD7N_65	T8	Bank 65	IO_L4N_T0U_N7_DBC_AD7N_65	FMC LA32_N
D41	IO_T3U_N12_65	Bank 65	K5	IO_T3U_N12_65	37	38	IO_L15P_T2L_N4_AD11P_65	N7	Bank 65	IO_L15P_T2L_N4_AD11P_65	SCL
SFP_RT_TX_Disable	IO_T1U_N12_65	Bank 65	H2	IO_T1U_N12_65	39	40	IO_L15N_T2L_N5_AD11N_65	N6	Bank 65	IO_L15N_T2L_N5_AD11N_65	SDA
BANK 65 POWER DC 1.8V	VCCO_65	Bank 65		VCCO_65	41	42	GND			Ground	Ground
D13	IO_L13P_T2L_N0_GC_QBC_65	Bank 65	L7	IO_L13P_T2L_N0_GC_QBC_65	43	44	IO_L6P_T0U_N10_AD6P_65	R6	Bank 65	IO_L6P_T0U_N10_AD6P_65	FMC LA33_P
D12	IO_L13N_T2L_N1_GC_QBC_65	Bank 65	L6	IO_L13N_T2L_N1_GC_QBC_65	45	46	IO_L6N_T0U_N11_AD6N_65	T6	Bank 65	IO_L6N_T0U_N11_AD6N_65	FMC LA33_N
FMC LA30_P	IO_L7P_T1L_N0_QBC_AD13P_65	Bank 65	L1	IO_L7P_T1L_N0_QBC_AD13P_6	47	48	IO_L16P_T2U_N6_QBC_AD3P_65	P7	Bank 65	IO_L16P_T2U_N6_QBC_AD3P_65	D11
FMC LA30_N	IO_L7N_T1L_N1_QBC_AD13N_65	Bank 65	K1	IO_L7N_T1L_N1_QBC_AD13N_6	49	50	IO_L16N_T2U_N7_QBC_AD3N_65	P6	Bank 65	IO_L16N_T2U_N7_QBC_AD3N_65	D10
Ground				GND	51	52	GND			Ground	Ground
D9	IO_L14P_T2L_N2_GC_65	Bank 65	M6	IO_L14P_T2L_N2_GC_65	53	54	IO_L2P_T0L_N2_65	U9	Bank 65	IO_L2P_T0L_N2_65	FMC LA31_P
D8	IO_L14N_T2L_N3_GC_65	Bank 65	L5	IO_L14N_T2L_N3_GC_65	55	56	IO_L2N_T0L_N3_65	V9	Bank 65	IO_L2N_T0L_N3_65	FMC LA31_N
FMC LA28_P	IO_L1P_T0L_N0_DBC_65	Bank 65	W8	IO_L1P_T0L_N0_DBC_65	57	58	IO_L12P_T1U_N10_GC_64	AE5	Bank 64	IO_L12P_T1U_N10_GC_64	SFP_LB_TX_Disable
FMC LA28_N	IO_L1N_T0L_N1_DBC_65	Bank 65	Y8	IO_L1N_T0L_N1_DBC_65	59	60	IO_L12N_T1U_N11_GC_64	AF5	Bank 64	IO_L12N_T1U_N11_GC_64	SFP_LT_TX_Disable
Ground				GND	61	62	GND			Ground	Ground
D7	IO_L24P_T3U_N10_64	Bank 64	AF1	IO_L24P_T3U_N10_64	63	64	IO_L21P_T3L_N4_AD8P_65	J7	Bank 65	IO_L21P_T3L_N4_AD8P_65	SCL_select
D6	IO_L24N_T3U_N11_64	Bank 64	AG1	IO_L24N_T3U_N11_64	65	66	IO_L21N_T3L_N5_AD8N_65	H7	Bank 65	IO_L21N_T3L_N5_AD8N_65	SDA_select
D5	IO_L18P_T2U_N10_AD2P_65	Bank 65	M8	IO_L18P_T2U_N10_AD2P_65	67	68	IO_L22P_T3U_N6_DBC_AD0P_64	AE2	Bank 64	IO_L22P_T3U_N6_DBC_AD0P_64	D3
D4	IO_L18N_T2U_N11_AD2N_65	Bank 65	L8	IO_L18N_T2U_N11_AD2N_65	69	70	IO_L22N_T3U_N7_DBC_AD0N_64	AF2	Bank 64	IO_L22N_T3U_N7_DBC_AD0N_64	D2
RTC POWER DC 1.2V-1.5V	VCC_PSBATT			VCC_PSBATT	71	72	GND			Ground	Ground
D1	IO_L23P_T3U_N8_64	Bank 64	AH2	IO_L23P_T3U_N8_64	73	74	IO_L10P_T1U_N6_QBC_AD4P_64	AG6	Bank 64	IO_L10P_T1U_N6_QBC_AD4P_64	HDMI/LCD_B0
PMOD2	IO_L23N_T3U_N9_64	Bank 64	AH1	IO_L23N_T3U_N9_64	75	76	IO_L10N_T1U_N7_QBC_AD4N_64	AG5	Bank 64	IO_L10N_T1U_N7_QBC_AD4N_64	HDMI/LCD_B1
D0	IO_L22P_T3U_N6_DBC_AD0P_65	Bank 65	K8	IO_L22P_T3U_N6_DBC_AD0P_6	77	78	IO_L14P_T2L_N2_GC_64	AC4	Bank 64	IO_L14P_T2L_N2_GC_64	HDMI/LCD_B2
D37	IO_L22N_T3U_N7_DBC_AD0N_65	Bank 65	K7	IO_L22N_T3U_N7_DBC_AD0N_6	79	80	IO_L14N_T2L_N3_GC_64	AC3	Bank 64	IO_L14N_T2L_N3_GC_64	HDMI/LCD_B3
Ground				GND	81	82	GND			Ground	Ground
ADC0_IN	IO_L20P_T3L_N2_AD1P_64	Bank 64	AG3	IO_L20P_T3L_N2_AD1P_64	83	84	IO_L16P_T2U_N6_QBC_AD3P_64	AD2	Bank 64	IO_L16P_T2U_N6_QBC_AD3P_64	ADC1_IN
PMOD2	IO_L20N_T3L_N3_AD1N_64	Bank 64	AH3	IO_L20N_T3L_N3_AD1N_64	85	86	IO_L16N_T2U_N7_QBC_AD3N_64	AD1	Bank 64	IO_L16N_T2U_N7_QBC_AD3N_64	HDMI/LCD_B4
FMC LA18_P_CC	IO_L12P_T1U_N10_GC_65	Bank 65	L3	IO_L12P_T1U_N10_GC_65	87	88	IO_L6P_T0U_N10_AD6P_64	AB6	Bank 64	IO_L6P_T0U_N10_AD6P_64	HDMI/LCD_B5
FMC LA18_N_CC	IO_L12N_T1U_N11_GC_65	Bank 65	L2	IO_L12N_T1U_N11_GC_65	89	90	IO_L6N_T0U_N11_AD6N_64	AC6	Bank 64	IO_L6N_T0U_N11_AD6N_64	HDMI/LCD_B6
Ground				GND	91	92	VCCO_64			VCCO_64	BANK 64 POWER DC 1.8V
FMC LA29_P	IO_L3P_T0L_N4_AD15P_65	Bank 65	U8	IO_L3P_T0L_N4_AD15P_65	93	94	IO_T0U_N12_VRP_64	AD6	Bank 64	IO_T0U_N12_VRP_64	HDMI/LCD_B7
FMC LA29_N	IO_L3N_T0L_N5_AD15N_65	Bank 65	V8	IO_L3N_T0L_N5_AD15N_65	95	96	IO_T1U_N12_64	AH6	Bank 64	IO_T1U_N12_64	HDMI/LCD_G0
FMC LA24_P	IO_L9P_T1L_N4_AD12P_65	Bank 65	K2	IO_L9P_T1L_N4_AD12P_65	97	98	IO_T2U_N12_64	AB5	Bank 64	IO_T2U_N12_64	HDMI/LCD_G1
FMC LA24_N	IO_L9N_T1L_N5_AD12N_65	Bank 65	J2	IO_L9N_T1L_N5_AD12N_65	99	100	IO_T3U_N12_64	AE4	Bank 64	IO_T3U_N12_64	HDMI/LCD_G2
Ground				GND	101	102	GND			Ground	Ground
PMOD2	IO_L13P_T2L_N0_GC_QBC_64	Bank 64	AD5	IO_L13P_T2L_N0_GC_QBC_64	103	104	IO_L15P_T2L_N4_AD11P_64	AB4	Bank 64	IO_L15P_T2L_N4_AD11P_64	ADC2_IN
PMOD2	IO_L13N_T2L_N1_GC_QBC_64	Bank 64	AD4	IO_L13N_T2L_N1_GC_QBC_64	105	106	IO_L15N_T2L_N5_AD11N_64	AB3	Bank 64	IO_L15N_T2L_N5_AD11N_64	HDMI/LCD_G3
FMC LA17_P_CC	IO_L11P_T1U_N8_GC_65	Bank 65	K4	IO_L11P_T1U_N8_GC_65	107	108	IO_L5P_T0U_N8_AD14P_65	R7	Bank 65	IO_L5P_T0U_N8_AD14P_65	FMC LA25_P
FMC LA17_N_CC	IO_L11N_T1U_N9_GC_65	Bank 65	K3	IO_L11N_T1U_N9_GC_65	109	110	IO_L5N_T0U_N9_AD14N_65	T7	Bank 65	IO_L5N_T0U_N9_AD14N_65	FMC LA25_N
Ground				GND	111	112	GND			Ground	Ground
ADC3_IN	IO_L19P_T3L_N0_DBC_AD9P_64	Bank 64	AG4	IO_L19P_T3L_N0_DBC_AD9P_6	113	114	IO_L10P_T1U_N6_QBC_AD4P_65	H4	Bank 65	IO_L10P_T1U_N6_QBC_AD4P_65	FMC LA26_P
PMOD2	IO_L19N_T3L_N1_DBC_AD9N_64	Bank 64	AH4	IO_L19N_T3L_N1_DBC_AD9N_6	115	116	IO_L10N_T1U_N7_QBC_AD4N_65	H3	Bank 65	IO_L10N_T1U_N7_QBC_AD4N_65	FMC LA26_N
PMOD2	IO_L21P_T3L_N4_AD8P_64	Bank 64	AE3	IO_L21P_T3L_N4_AD8P_64	117	118	IO_L11P_T1U_N8_GC_64	AF7	Bank 64	IO_L11P_T1U_N8_GC_64	HDMI/LCD_G4
PMOD2	IO_L21N_T3L_N5_AD8N_64	Bank 64	AF3	IO_L21N_T3L_N5_AD8N_64	119	120	IO_L11N_T1U_N9_GC_64	AF6	Bank 64	IO_L11N_T1U_N9_GC_64	HDMI/LCD_G5
Ground				GND	121	122	GND			Ground	Ground
PMOD1	IO_L19P_T3L_N0_DBC_AD9P_65	Bank 65	J5	IO_L19P_T3L_N0_DBC_AD9P_6	123	124	IO_L5P_T0U_N8_AD14P_64	AB7	Bank 64	IO_L5P_T0U_N8_AD14P_64	HDMI/LCD_G6
PMOD1	IO_L19N_T3L_N1_DBC_AD9N_65	Bank 65	J4	IO_L19N_T3L_N1_DBC_AD9N_6	125	126	IO_L5N_T0U_N9_AD14N_64	AC7	Bank 64	IO_L5N_T0U_N9_AD14N_64	HDMI/LCD_DCLK
PMOD1	IO_L17P_T2U_N8_AD10P_65	Bank 65	N9	IO_L17P_T2U_N8_AD10P_65	127	128	IO_L3P_T0L_N4_AD15P_64	AB8	Bank 64	IO_L3P_T0L_N4_AD15P_64	HDMI/LCD_G7
PMOD1	IO_L17N_T2U_N9_AD10N_65	Bank 65	N8	IO_L17N_T2U_N9_AD10N_65	129	130	IO_L3N_T0L_N5_AD15N_64	AC8	Bank 64	IO_L3N_T0L_N5_AD15N_64	HDMI/LCD_R0
Ground				GND	131	132	GND			Ground	Ground
PMOD1	IO_L23P_T3U_N8_I2C_SCLK_65	Bank 65	K9	IO_L23P_T3U_N8_I2C_SCLK_6	133	134	IO_L4P_T0U_N6_DBC_AD7P_64	AD7	Bank 64	IO_L4P_T0U_N6_DBC_AD7P_64	HDMI/LCD_R1
PMOD1	IO_L23N_T3U_N9_65	Bank 65	J9	IO_L23N_T3U_N9_65	135	136	IO_L4N_T0U_N7_DBC_AD7N_64	AE7	Bank 64	IO_L4N_T0U_N7_DBC_AD7N_64	HDMI/LCD_R2
FMC LA27_P	IO_L8P_T1L_N2_AD5P_65	Bank 65	J1	IO_L8P_T1L_N2_AD5P_65	137	138	IO_L7P_T1L_N0_QBC_AD13P_64	AG9	Bank 64	IO_L7P_T1L_N0_QBC_AD13P_64	HDMI/LCD_R3
FMC LA27_N	IO_L8N_T1L_N3_AD5N_65	Bank 65	H1	IO_L8N_T1L_N3_AD5N_65	139	140	IO_L7N_T1L_N1_QBC_AD13N_64	AH9	Bank 64	IO_L7N_T1L_N1_QBC_AD13N_64	HDMI/LCD_R4
Ground				GND	141	142	GND			Ground	Ground
PMOD1	IO_L20P_T3L_N2_AD1P_65	Bank 65	J6	IO_L20P_T3L_N2_AD1P_65	143	144	IO_L24P_T3U_N10_PERSTN1_I2C_SDA_65	H9	Bank 65	IO_L24P_T3U_N10_PERSTN1_I2C_SDA_65	HDMI/LCD_R5
PMOD1	IO_L20N_T3L_N3_AD1N_65	Bank 65	H6	IO_L20N_T3L_N3_AD1N_65	145	146	IO_L24N_T3U_N11_PERSTN0_65	H8	Bank 65	IO_L24N_T3U_N11_PERSTN0_65	HDMI/LCD_R6
ADC4_IN	IO_L18P_T2U_N10_AD2P_64	Bank 64	AB1	IO_L18P_T2U_N10_AD2P_64	147	148	IO_L8P_T1L_N2_AD5P_64	AF8	Bank 64	IO_L8P_T1L_N2_AD5P_64	HDMI/LCD_R7
PMOD2	IO_L18N_T2U_N11_AD2N_64	Bank 64	AC1	IO_L18N_T2U_N11_AD2N_64	149	150	IO_L8N_T1L_N3_AD5N_64	AG8	Bank 64	IO_L8N_T1L_N3_AD5N_64	HDMI/LCD_DE
Ground				GND	151	152	GND			Ground	Ground
ADC5_IN	IO_L17P_T2U_N8_AD10P_64	Bank 64	AB2	IO_L17P_T2U_N8_AD10P_64	153	154	IO_L2P_T0L_N2_64	AE9	Bank 64	IO_L2P_T0L_N2_64	HDMI/LCD_HSYNC
LCD_TP_INTn	IO_L17N_T2U_N9_AD10N_64	Bank 64	AC2	IO_L17N_T2U_N9_AD10N_64	155	156	IO_L2N_T0L_N3_64	AE8	Bank 64	IO_L2N_T0L_N3_64	HDMI/LCD_VSYNC
LCD_TP_RESET	IO_L9P_T1L_N4_AD12P_64	Bank 64	AH8	IO_L9P_T1L_N4_AD12P_64	157	158	IO_L1P_T0L_N0_DBC_64	AC9	Bank 64	IO_L1P_T0L_N0_DBC_64	HDMI_intn
HDMI_RESET/LCD_PWR_EN	IO_L9N_T1L_N5_AD12N_64	Bank 64	AH7	IO_L9N_T1L_N5_AD12N_64	159	160	IO_L1N_T0L_N1_DBC_64	AD9	Bank 64	IO_L1N_T0L_N1_DBC_64	LCD_BL_CN

Function	SoC Pin Name	Bank	Ball	Net Name	J3		Net Name	Ball	Bank	SoC Pin Name	Function
S1	IO_T0U_N12_VRP_66	Bank 66	G4	IO_T0U_N12_VRP_66	1	2	VCC_IN_COM			DC 3.3V	SOM POWER DC 3.3V
CAN_TX	IO_T2U_N12_66	Bank 66	E7	IO_T2U_N12_66	3	4	VCC_IN_COM			DC 3.3V	SOM POWER DC 3.3V
FMC PRSNT_M2C_L	IO_T1U_N12_66	Bank 66	D2	IO_T1U_N12_66	5	6	GND			Ground	Ground
CAN_RX	IO_T3U_N12_66	Bank 66	C7	IO_T3U_N12_66	7	8	USER_CLKIN_P			USER_CLKIN_P	SI5338 refence clk in
Ground				GND	9	10	USER_CLKIN_N			USER_CLKIN_N	SI5338 refence clk in
FMC LA09_P	IO_L8P_T1L_N2_AD5P_66	Bank 66	A2	IO_L8P_T1L_N2_AD5P_66	11	12	GND			Ground	Ground
FMC LA09_N	IO_L8N_T1L_N3_AD5N_66	Bank 66	A1	IO_L8N_T1L_N3_AD5N_66	13	14	USER_CLKOUT_P			USER_CLKOUT_P	SI5338 clk out
FMC LA10_P	IO_L9P_T1L_N4_AD12P_66	Bank 66	B3	IO_L9P_T1L_N4_AD12P_66	15	16	USER_CLKOUT_N			USER_CLKOUT_N	SI5338 clk out
FMC LA10_N	IO_L9N_T1L_N5_AD12N_66	Bank 66	A3	IO_L9N_T1L_N5_AD12N_66	17	18	GND			Ground	Ground
Ground				GND	19	20	IO_L13P_T2L_N0_GC_QBC_66	D7	Bank 66	IO_L13P_T2L_N0_GC_QBC_66	FMC CLK0_M2C_P
FMC LA11_P	IO_L5P_T0U_N8_AD14P_66	Bank 66	E4	IO_L5P_T0U_N8_AD14P_66	21	22	IO_L13N_T2L_N1_GC_QBC_66	D6	Bank 66	IO_L13N_T2L_N1_GC_QBC_66	FMC CLK0_M2C_N
FMC LA11_N	IO_L5N_T0U_N9_AD14N_66	Bank 66	E3	IO_L5N_T0U_N9_AD14N_66	23	24	IO_L10P_T1U_N6_QBC_AD4P_66	B4	Bank 66	IO_L10P_T1U_N6_QBC_AD4P_66	FMC LA06_P
FMC LA13_P	IO_L4P_T0U_N6_DBC_AD7P_66	Bank 66	G3	IO_L4P_T0U_N6_DBC_AD7P_66	25	26	IO_L10N_T1U_N7_QBC_AD4N_66	A4	Bank 66	IO_L10N_T1U_N7_QBC_AD4N_66	FMC LA06_N
FMC LA13_N	IO_L4N_T0U_N7_DBC_AD7N_66	Bank 66	F3	IO_L4N_T0U_N7_DBC_AD7N_66	27	28	GND			Ground	Ground
Ground				GND	29	30	IO_L2P_T0L_N2_66	E1	Bank 66	IO_L2P_T0L_N2_66	FMC LA05_P
FMC LA00_P_CC	IO_L14P_T2L_N2_GC_66	Bank 66	E5	IO_L14P_T2L_N2_GC_66	31	32	IO_L2N_T0L_N3_66	D1	Bank 66	IO_L2N_T0L_N3_66	FMC LA05_N
FMC LA00_N_CC	IO_L14N_T2L_N3_GC_66	Bank 66	D5	IO_L14N_T2L_N3_GC_66	33	34	IO_L3P_T0L_N4_AD15P_66	F2	Bank 66	IO_L3P_T0L_N4_AD15P_66	FMC LA08_P
FMC LA01_P_CC	IO_L12P_T1U_N10_GC_66	Bank 66	C3	IO_L12P_T1U_N10_GC_66	35	36	IO_L3N_T0L_N5_AD15N_66	E2	Bank 66	IO_L3N_T0L_N5_AD15N_66	FMC LA08_N
FMC LA01_N_CC	IO_L12N_T1U_N11_GC_66	Bank 66	C2	IO_L12N_T1U_N11_GC_66	37	38	GND			Ground	Ground
Ground				GND	39	40	IO_L19P_T3L_N0_DBC_AD9P_66	B5	Bank 66	IO_L19P_T3L_N0_DBC_AD9P_66	FMC LA03_P
FMC LA16_P	IO_L6P_T0U_N10_AD6P_66	Bank 66	G5	IO_L6P_T0U_N10_AD6P_66	41	42	IO_L19N_T3L_N1_DBC_AD9N_66	A5	Bank 66	IO_L19N_T3L_N1_DBC_AD9N_66	FMC LA03_N
FMC LA16_N	IO_L6N_T0U_N11_AD6N_66	Bank 66	F5	IO_L6N_T0U_N11_AD6N_66	43	44	IO_L1P_T0L_N0_DBC_66	G1	Bank 66	IO_L1P_T0L_N0_DBC_66	FMC LA07_P
FMC LA15_P	IO_L7P_T1L_N0_QBC_AD13P_66	Bank 66	C1	IO_L7P_T1L_N0_QBC_AD13P_6	45	46	IO_L1N_T0L_N1_DBC_66	F1	Bank 66	IO_L1N_T0L_N1_DBC_66	FMC LA07_N
FMC LA15_N	IO_L7N_T1L_N1_QBC_AD13N_66	Bank 66	B1	IO_L7N_T1L_N1_QBC_AD13N_6	47	48	GND			Ground	Ground
Ground				GND	49	50	IO_L20P_T3L_N2_AD1P_66	C6	Bank 66	IO_L20P_T3L_N2_AD1P_66	FMC LA04_P
FMC LA14_P	IO_L23P_T3U_N8_66	Bank 66	A9	IO_L23P_T3U_N8_66	51	52	IO_L20N_T3L_N3_AD1N_66	B6	Bank 66	IO_L20N_T3L_N3_AD1N_66	FMC LA04_N
FMC LA14_N	IO_L23N_T3U_N9_66	Bank 66	A8	IO_L23N_T3U_N9_66	53	54	IO_L11P_T1U_N8_GC_66	D4	Bank 66	IO_L11P_T1U_N8_GC_66	FMC CLK0_C2M_P
FMC LA20_P	IO_L15P_T2L_N4_AD11P_66	Bank 66	G6	IO_L15P_T2L_N4_AD11P_66	55	56	IO_L11N_T1U_N9_GC_66	C4	Bank 66	IO_L11N_T1U_N9_GC_66	FMC CLK0_C2M_N
FMC LA20_N	IO_L15N_T2L_N5_AD11N_66	Bank 66	F6	IO_L15N_T2L_N5_AD11N_66	57	58	GND			Ground	Ground
Ground				GND	59	60	IO_L21P_T3L_N4_AD8P_66	A7	Bank 66	IO_L21P_T3L_N4_AD8P_66	FMC LA02_P
FMC LA19_P	IO_L24P_T3U_N10_66	Bank 66	C9	IO_L24P_T3U_N10_66	61	62	IO_L21N_T3L_N5_AD8N_66	A6	Bank 66	IO_L21N_T3L_N5_AD8N_66	FMC LA02_N
FMC LA19_N	IO_L24N_T3U_N11_66	Bank 66	B9	IO_L24N_T3U_N11_66	63	64	IO_L22P_T3U_N6_DBC_AD0P_66	C8	Bank 66	IO_L22P_T3U_N6_DBC_AD0P_66	FMC LA12_P
FMC LA23_P	IO_L18P_T2U_N10_AD2P_66	Bank 66	E9	IO_L18P_T2U_N10_AD2P_66	65	66	IO_L22N_T3U_N7_DBC_AD0N_66	B8	Bank 66	IO_L22N_T3U_N7_DBC_AD0N_66	FMC LA12_N
FMC LA23_N	IO_L18N_T2U_N11_AD2N_66	Bank 66	D9	IO_L18N_T2U_N11_AD2N_66	67	68	GND			Ground	Ground
Ground				GND	69	70	IO_L17P_T2U_N8_AD10P_66	F8	Bank 66	IO_L17P_T2U_N8_AD10P_66	FMC LA22_P
USB Data plus	USB_PHY_DP			USB_PHY_DP	71	72	IO_L17N_T2U_N9_AD10N_66	E8	Bank 66	IO_L17N_T2U_N9_AD10N_66	FMC LA22_N
USB Data minus	USB_PHY_DM			USB_PHY_DM	73	74	IO_L16P_T2U_N6_QBC_AD3P_66	G8	Bank 66	IO_L16P_T2U_N6_QBC_AD3P_66	FMC LA21_P
PSMIO POWER DC 1.8V				VCC_PSMIO_COM	75	76	IO_L16N_T2U_N7_QBC_AD3N_66	F7	Bank 66	IO_L16N_T2U_N7_QBC_AD3N_66	FMC LA21_N
USB OTG ID	USB_PHY_ID			USB_PHY_ID	77	78	VCCO_66			VCCO_66	BANK 66 POWER DC 1.8V
USB VBUS	USB_PHY_VBUS			USB_PHY_VBUS	79	80	ETH_MD1_P			ETH_MD1_P	Ethernet Data 1 Positive
USB Active high VBUS control output	VBUS_SW_EN			VBUS_SW_EN	81	82	ETH_MD1_N			ETH_MD1_N	Ethernet Data 1 Negative
DP_DOUT	PS_MIO27	Bank 501	J15	PS_MIO27	83	84	GND			Ground	Ground
DP_HP_DET	PS_MIO28	Bank 501	K15	PS_MIO28	85	86	ETH_MD2_P			ETH_MD2_P	Ethernet Data 2 Positive
NC	PS_MIO36	Bank 501	K17	PS_MIO36	87	88	ETH_MD2_N			ETH_MD2_N	Ethernet Data 2 Negative
NC	PS_MIO32	Bank 501	J16	PS_MIO32	89	90	GND			Ground	Ground
PCIE_PERST	PS_MIO31	Bank 501	H16	PS_MIO31	91	92	ETH_MD3_P			ETH_MD3_P	Ethernet Data 3 Positive
NC	PS_MIO33	Bank 501	L16	PS_MIO33	93	94	ETH_MD3_N			ETH_MD3_N	Ethernet Data 3 Negative
NC	PS_MIO37	Bank 501	J17	PS_MIO37	95	96	GND			Ground	Ground
DP_DIN	PS_MIO30	Bank 501	F16	PS_MIO30	97	98	ETH_MD4_P			ETH_MD4_P	Ethernet Data 4 Positive
DP_OE	PS_MIO29	Bank 501	G16	PS_MIO29	99	100	ETH_MD4_N			ETH_MD4_N	Ethernet Data 4 Negative
USB_UART_TXD	DEBUG_TXD	Bank 501	H17	DEBUG_TXD	101	102	GND			Ground	Ground
USB_UART_RXD	DEBUG_RXD	Bank 501	L17	DEBUG_RXD	103	104	ETH_LED_LINK			ETH_LED_LINK	Ethernet Status LED
SDIO Data 3	SD1_D3	Bank 501	M18	SD1_D3	105	106	ETH_LED_ACT			ETH_LED_ACT	Ethernet Link LED
SDIO Card Detection	SD1_CD	Bank 501	K20	SD1_CD	107	108	PS_LPD_EN	J16	Bank 501	**PS_LPD_EN**	**No connect,leave it floating**
SDIO Write Protected	SD1_WP	Bank 501	J20	SD1_WP	109	110	PS_FPD_EN	L16	Bank 501	**PS_FPD_EN**	**No connect,leave it floating**
SDIO Data 1	SD1_D1	Bank 501	H21	SD1_D1	111	112	PL_PD_EN	K17	Bank 501	**PL_PD_EN**	**System power on/off**
SDIO Command	SD1_CMD	Bank 501	M19	SD1_CMD	113	114	PS_VCCO_EN			PS_VCCO_EN	PS POWER OK
SDIO Data 2	SD1_D2	Bank 501	J21	SD1_D2	115	116	PL_VCCO_EN			PL_VCCO_EN	PL POWER OK
SDIO Data 0	SD1_D0	Bank 501	L20	SD1_D0	117	118	PL_VCU_EN	J17	Bank 501	PL_VCU_EN	PL_VCU POWER OK
SDIO Clock	SD1_CLK	Bank 501	L21	SD1_CLK	119	120	SYS_RESET_IN	N19	Bank 503	SYS_RESET_IN	SYSTEM RESET
JTAG Test Mode Select	PS_JTAG_TMS	Bank 503	N21	PS_JTAG_TMS	121	122	PS_PROG_B	R17	Bank 503	PS_PROG_B	configuration block RESET
JTAG Test Data Output	PS_JTAG_TDO	Bank 503	T21	PS_JTAG_TDO	123	124	PS_MODE0			PS_MODE0	JTAG_MODE
JTAG Test Data Input	PS_JTAG_TDI	Bank 503	R18	PS_JTAG_TDI	125	126	PS_MODE1			PS_MODE1	JTAG_MODE
JTAG Test Clock	PS_JTAG_TCK	Bank 503	R19	PS_JTAG_TCK	127	128	PS_MODE2			PS_MODE2	JTAG_MODE
Ground				GND	129	130	GND			Ground	Ground
PCIE	PS_MGTRTXP0_505	Bank 505	E25	PS_MGTRTXP0_505	131	132	PS_MGTRTXP1_505	D23	Bank 505	PS_MGTRTXP1_505	SATA
PCIE	PS_MGTRTXN0_505	Bank 505	E26	PS_MGTRTXN0_505	133	134	PS_MGTRTXN1_505	D24	Bank 505	PS_MGTRTXN1_505	SATA
Ground				GND	135	136	GND			Ground	Ground
USB3.0	PS_MGTRTXP2_505	Bank 505	C25	PS_MGTRTXP2_505	137	138	PS_MGTRRXP3_505	A25	Bank 505	PS_MGTRRXP3_505	PS_MGTRRXP3_505
USB3.0	PS_MGTRTXN2_505	Bank 505	C26	PS_MGTRTXN2_505	139	140	PS_MGTRRXN3_505	A26	Bank 505	PS_MGTRRXN3_505	PS_MGTRRXN3_505
Ground				GND	141	142	GND			Ground	Ground
SATA	PS_MGTRRXP1_505	Bank 505	D27	PS_MGTRRXP1_505	143	144	PS_MGTRTXP3_505	B23	Bank 505	PS_MGTRTXP3_505	DP display
SATA	PS_MGTRRXN1_505	Bank 505	D28	PS_MGTRRXN1_505	145	146	PS_MGTRTXN3_505	B24	Bank 505	PS_MGTRTXN3_505	DP display
Ground				GND	147	148	GND			Ground	Ground
PCIE	PS_MGTRRXP0_505	Bank 505	F27	PS_MGTRRXP0_505	149	150	PS_MGTRRXP2_505	B27	Bank 505	PS_MGTRRXP2_505	USB3.0
PCIE	PS_MGTRRXN0_505	Bank 505	F28	PS_MGTRRXN0_505	151	152	PS_MGTRRXN2_505	B28	Bank 505	PS_MGTRRXN2_505	USB3.0
Ground				GND	153	154	GND			Ground	Ground
100M_CLOCK	PS_MGTREFCLK0P_505	Bank 505	F23	PS_MGTREFCLK0P_505	155	156	PS_MGTREFCLK1P_505	E21	Bank 505	PS_MGTREFCLK1P_505	125M_clock
100M_CLOCK	PS_MGTREFCLK0N_505	Bank 505	F24	PS_MGTREFCLK0N_505	157	158	PS_MGTREFCLK1N_505	E22	Bank 505	PS_MGTREFCLK1N_505	125M_clock
Ground				GND	159	160	GND			Ground	Ground

GND　VCC　Bank 224

Bank 65　Bank 64　Bank 66

Bank 505　Bank 501　Bank 503

Other