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# MYC-YF13X Pin List

Ver: 1.0
2023.5.13

| MYC-YF135 Pin List (STM32MP135DAF7) V1.0 | | | Alternate Funtion AF0 to AF14 | | | | | Alternate Funtion AF0 to AF5 | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Pin Number | Signal Name | Port Number | AF0 | AF1 | AF2 | AF3 | AF4 | AF5 | AF6 | AF7 | AF8 | AF9 | AF10 | AF11 | AF12 | AF13 | AF14 |
| 1 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 2 | DCMIPP_D0 | PA9 | / | TIM1_CH2 | / | / | I2C3_SMBA | / | DFSDM1_DATIN0 | USART1_TX | UART4_TX | / | FMC_NWAIT | / | / | DCMIPP_D0 | LTDC_R6 |
| 3 | DCMIPP_D4 | PE13 | / | TIM1_CH3 | / | / | I2C5_SDA | I2S4_SDI/SPI4_MISO | / | / | / | / | / | LTDC_B1 | FMC_D10/FMC_DA10 | DCMIPP_D4 | LTDC_R6 |
| 4 | DCMIPP_D1 | PB0 | / | / | SAI1_MCLK_A | / | / | / | SAI1_CK1 | / | / | FDCAN1_RX | / | / | FMC_B2/FMC_DA2 | DCMIPP_D1 | / |
| 5 | DCMIPP_D5 | PD3 | / | / | TIM2_CH1 | USART2_CTS/USART2_NSS | DFSDM1_CKOUT | I2C1_SDA | SAI1_D3 | / | / | / | / | / | FMC_CLK | DCMIPP_D5 | / |
| 6 | DCMIPP_D7 | PE9 | / | TIM1_CH1 | / | / | / | / | / | / | / | QUADSPI_BK1_IO1 | / | LTDC_HSYNC | FMC_D5/FMC_DA5 | DCMIPP_D7 | LTDC_R7 |
| 7 | DCMIPP_D2 | PH14 | / | / | / | DFSDM1_DATIN2 | I2C3_SDA | / | DCMIPP_D8 | / | UART4_RX | / | / | LTDC_B4 | / | DCMIPP_D2 | DCMIPP_PIXCLK |
| 8 | DCMIPP_D6 | PB8 | / | TIM16_CH1 | TIM4_CH3 | / | I2C1_SCL | I2C3_SCL | DFSDM1_DATIN1 | / | UART4_RX | / | SAI1_D1 | / | FMC_D13/FMC_DA13 | DCMIPP_D6 | / |
| 9 | DCMIPP_D3 | PE4 | / | SPI5_MISO | SAI1_D2 | DFSDM1_DATIN3 | TIM15_CH1N | I2S_CKIN | SAI1_FS_A | UART7_DE/UART7_RTS | UART8_TX | QUADSPI_BK2_NCS | FMC_NCE2 | / | FMC_A25 | DCMIPP_D3 | LTDC_G7 |
| 10 | DCMIPP_PIXCLK | PB7 | / | TIM17_CH1N | TIM4_CH2 | / | / | I2S4_CK | I2C4_SDA | / | / | / | FMC_NCE2 | / | FMC_NL | DCMIPP_D13 | DCMIPP_PIXCLK |
| 11 | DCMIPP_VS | PG9 | DEBUG_DBTRGO | / | / | / | I2C2_SDA | / | / | USART6_RX | SPDIFRX_IN3 | FDCAN1_RX | FMC_NE2 | / | FMC_NCE | DCMIPP_VSYNC | / |
| 12 | DCMIPP_HS | PB8 | DEBUG_TRACED9 | / | TIM5_ETR | USART2_RX | I2C3_SDA | / | / | / | / | / | / | LTDC_R6 | FMC_A8 | DCMIPP_HSYNC | LTDC_R2 |
| 13 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 14 | VSSM_SV | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 15 | VSSM_SV | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 16 | VDD_SV3 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 17 | VBAT | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 18 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 19 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 20 | ETH2_MD10 | PB6 | DEBUG_TRACED6 | TIM16_CH1N | TIM4_CH1 | TIM8_CH1 | USART1_TX | / | SAI1_CK2 | LTDC_B6 | / | QUADSPI_BK1_NCS | / | ETH2_MD10 | FMC_NE3 | DCMIPP_D5 | LTDC_B7 |
| 21 | ETH2_MDC | PG5 | / | TIM17_CH1 | / | / | / | / | / | / | / | / | ETH2_MDC | LTDC_G4 | FMC_A15 | DCMIPP_VSYNC | DCMIPP_D3 |
| 22 | ETH2_TXCTL | PF6 | / | TIM16_CH1 | / | / | / | SPI5_NSS | / | UART7_RX | / | QUADSPI_BK1_IO2 | / | ETH2_TX_CTL/ETH2_TX_EN | / | LTDC_R7 | LTDC_G4 |
| 23 | ETH2_TXD3 | PB9 | RCC_MCO_2 | TIM1_BKIN2 | SAI2_SCK_B | / | TIM15_CH2 | I2C3_SMBA | SAI1_SCK_B | / | UART4_DE/UART4_RTS | / | / | ETH2_TXD3 | FMC_A22 | DCMIPP_D7 | LTDC_G3 |
| 24 | ETH2_TXD2 | PG1 | / | LPTIM1_ETR | TIM4_ETR | SAI2_FS_A | I2C2_SMBA | I2S2_SDI/SPI2_MISO | SAI2_D2 | / | / | FDCAN2_TX | ETH2_TXD2 | / | FMC_NBL0 | / | LTDC_G7 |
| 25 | ETH2_TXD1 | PG11 | / | / | / | / | SAI2_D3 | I2S2_MCK | / | USART3_TX | UART4_TX | / | ETH2_TXD1 | / | FMC_A24 | DCMIPP_D14 | LTDC_B2 |
| 26 | ETH2_TXD0 | PB7 | / | TIM17_CH1 | / | / | / | / | / | UART7_TX | UART4_CTS | / | ETH1_CLK125 | ETH2_TXD0 | FMC_A18 | / | LTDC_G2 |
| 27 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 28 | ETH2_TXC | PG3 | / | / | / | TIM8_BKIN2 | I2C2_SDA | / | SAI2_SD_B | / | / | FDCAN2_RX | ETH2_GTX_CLK | ETH1_MDIO | FMC_A13 | DCMIPP_D15 | DCMIPP_D12 |
| 29 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 30 | ETH2_RXCTL | PA12 | / | TIM1_ETR | SAI2_MCLK_A | / | / | / | / | USART1_DE/USART1_RTS | / | / | / | ETH2_CRS_DV/ETH2_RX_CTL/ETH2_RX_DV | FMC_A7 | DCMIPP_D1 | LTDC_G6 |
| 31 | ETH2_RXC | PH11 | / | SPI5_NSS | TIM5_CH2 | SAI2_SD_A | / | I2S2_WS/SPI2_NSS | I2C4_SCL | USART6_RX | / | QUADSPI_BK2_IO0 | / | ETH2_REF_CLK/ETH2_RX_CLK | FMC_A12 | / | LTDC_G6 |
| 32 | ETH2_RXD2 | PB6 | / | / | TIM12_CH1 | USART2_CK | I2C5_SDA | I2S2_CK/SPI2_SCK | / | / | / | QUADSPI_BK1_IO2 | ETH1_PHY_INTN | ETH1_RX_ER | ETH2_RXD2 | QUADSPI_BK1_NCS | / |
| 33 | ETH2_RXD3 | PA8 | RCC_MCO_1 | / | SAI2_MCLK_A | TIM8_BKIN2 | I2C4_SDA | SPI5_MISO | SAI2_CK1 | USART1_CK | I2S2_SDO/SPI2_MOSI | / | USB_OTG_HS_SOF | ETH2_RXD3 | FMC_A21 | / | LTDC_B7 |
| 34 | ETH2_RXD0 | PF4 | / | / | / | USART2_RX | / | / | / | / | / | / | / | ETH2_RXD0 | FMC_A4 | DCMIPP_D4 | LTDC_B6 |
| 35 | ETH2_RXD1 | PE2 | DEBUG_TRACECLK | TIM2_ETR | / | / | I2C4_SCL | SPI5_MOSI | SAI1_FS_B | USART6_DE/USART6_RTS | / | SPDIFRX_IN1 | ETH2_RXD1 | / | FMC_A23 | / | LTDC_R1 |
| 36 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 37 | ETH2_CLK125 | PB2 | / | LPTIM1_IN2 | / | / | / | / | DCMIPP_D9 | LTDC_G1 | UART7_TX | QUADSPI_BK2_IO0 | ETH2_CRS | ETH1_CRS | FMC_NE4 | ETH2_CLK125 | LTDC_B0 |
| 38 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 39 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 40 | SPI5_SCK | PG10 | / | / | / | / | / | SPI5_SCK | SAI1_SD_B | / | UART8_CTS | FDCAN1_TX | QUADSPI_BK2_IO1 | / | FMC_NE3 | DCMIPP_D2 | / |
| 41 | SPI5_MOSI | PH12 | / | USART2_TX | TIM5_CH3 | DFSDM1_CKIN1 | I2C5_SCL | SPI5_MOSI | SAI1_SCK_A | / | / | QUADSPI_BK2_IO2 | ETH1_CRS | / | FMC_A6 | DCMIPP_D3 | / |
| 42 | SPI5_MISO | PG8 | / | TIM2_CH1 | / | TIM8_ETR | / | SPI5_MISO | SAI1_MCLK_B | LTDC_B1 | USART3_DE/USART3_RTS | SPDIFRX_IN2 | QUADSPI_BK2_IO2 | QUADSPI_BK1_IO3 | FMC_NE2 | ETH2_CLK | DCMIPP_D6 |
| 43 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 44 | ETH1_CLK125 | PF12 | / | / | / | / | / | I2S1_WS/SPI1_NSS | SAI1_SD_A | / | UART4_TX | / | ETH1_TX_ER | ETH1_CLK125 | / | / | / |
| 45 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 46 | ETH1_MDC | PG2 | / | RCC_MCO_2 | / | TIM8_BKIN | / | / | / | / | / | / | SAI2_MCLK_B | ETH1_MDC | / | DCMIPP_D1 | / |
| 47 | ETH1_MDIO | PA2 | / | TIM2_CH3 | TIM5_CH3 | LPTIM4_OUT | TIM15_CH1 | / | / | USART2_TX | / | / | / | ETH1_MDIO | / | / | / |
| 48 | ETH1_RXC | PA1 | / | TIM2_CH2 | TIM5_CH2 | LPTIM5_OUT | TIM15_CH1N | / | DFSDM1_CKIN0 | USART2_DE/USART2_RTS | / | / | / | ETH1_REF_CLK/ETH1_RX_CLK | / | / | / |
| 49 | ETH1_RXD2 | PB0 | DEBUG_DBTRGI | TIM1_CH2N | TIM3_CH3 | TIM8_CH2N | USART1_RX | I2S1_MCK | SAI2_FS_A | USART1_CK | UART4_CTS | / | SAI2_D2 | ETH1_RXD2 | / | / | / |
| 50 | ETH1_RXD1 | PC5 | / | / | / | DFSDM1_DATIN2 | SAI2_D4 | I2S_CKIN | SAI1_D4 | USART2_CTS/USART2_NSS | / | SPDIFRX_IN3 | / | ETH1_RXD1 | / | / | / |
| 51 | ETH1_RXD3 | PB1 | / | TIM1_CH3N | TIM3_CH4 | TIM8_CH3N | / | I2S1_CK/SPI1_SCK | DFSDM1_DATIN1 | UART4_RX | / | / | / | ETH1_RXD3 | / | / | / |
| 52 | ETH1_RXCTL | PA7 | / | TIM1_CH1N | TIM3_CH2 | TIM8_CH1N | SAI2_D1 | I2S1_CK/SPI1_SCK | / | USART1_CTS/USART1_NSS | / | TIM14_CH1 | / | ETH1_CRS_DV/ETH1_RX_CTL/ETH1_RX_DV | SAI2_SD_A | / | / |
| 53 | ETH1_RXD0 | PC4 | / | / | TIM3_ETR | DFSDM1_CKIN2 | SAI1_D3 | I2S1_MCK | / | / | UART5_DE/UART5_RTS | SPDIFRX_IN2 | / | ETH1_RXD0 | SAI2_D3 | / | / |
| 54 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 55 | ETH1_TXC | PC1 | / | / | / | DFSDM1_DATIN0 | / | / | SAI1_D3 | / | / | / | ETH1_CRS_DV/ETH1_RX_DV | ETH1_GTX_CLK | / | / | / |
| 56 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 57 | ETH1_TXD1 | PG14 | / | LPTIM1_ETR | / | / | / | / | SAI2_D1 | USART6_TX | / | / | SAI2_SD_A | ETH1_TXD1 | / | / | / |
| 58 | ETH1_TXD2 | PC2 | / | SPI5_NSS | / | / | / | I2S1_WS/SPI1_NSS | SAI2_MCLK_A | USART1_DE/USART1_RTS | / | / | SAI2_CK1 | ETH1_TXD2 | / | / | / |
| 59 | ETH1_TXD0 | PG13 | / | LPTIM1_OUT | / | / | / | / | / | USART6_CTS/USART6_NSS | / | / | / | ETH1_TXD0 | / | / | / |
| 60 | ETH1_TXCTL | PB11 | / | TIM2_CH4 | / | LPTIM1_OUT | I2C5_SMBA | / | / | USART3_RX | / | / | / | ETH1_TX_CTL/ETH1_TX_EN | / | / | / |
| 61 | ETH1_TXD3 | PE5 | / | / | SAI2_SCK_B | TIM8_CH3 | TIM15_CH1 | / | / | / | UART4_RX | / | ETH1_TXD3 | / | FMC_NE1 | / | / |
| 62 | NC | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 63 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 64 | UART4_RX | PD8 | / | / | / | USART2_TX | / | I2S4_WS | / | USART3_TX | UART4_RX | / | / | / | / | DCMIPP_D9 | DCMIPP_D3 |
| 65 | UART4_TX | PD6 | / | TIM16_CH1N | SAI1_D1 | / | / | / | SAI1_SD_A | USART3_RX | UART4_TX | / | / | / | / | DCMIPP_D4 | DCMIPP_D0 |
| 66 | UART5_RX | PF13 | / | TIM2_ETR | SAI1_MCLK_B | / | / | / | DFSDM1_DATIN3 | USART2_TX | UART5_RX | / | / | / | / | / | / |
| 67 | UART5_TX | PA0 | / | TIM2_CH1 | TIM5_CH1 | TIM8_ETR | TIM15_BKIN | / | SAI1_SD_B | / | UART5_TX | / | / | ETH2_CRS | ETH2_CRS | / | / |
| 68 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 69 | USB_B2_P | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 70 | USB_B2_N | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 71 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 72 | USB_D1_P | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 73 | USB_D1_N | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 74 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 75 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 76 | I2S1_MCLK | PC0 | / | / | SAI1_SCK_A | / | SAI1_CK2 | I2S1_MCK | I2S1_SDO/SPI1_MOSI | USART1_TX | / | / | / | / | / | / | / |
| 77 | I2S1_SDI | PA6 | / | TIM1_BKIN | TIM3_CH1 | TIM8_BKIN | SAI2_CK2 | I2S1_SDI/SPI1_MISO | / | USART1_CK | UART4_DE/UART4_RTS | TIM13_CH1 | / | / | SAI2_SCK_A | / | / |
| 78 | I2S1_SDO | PA3 | / | TIM2_CH4 | TIM5_CH4 | LPTIM5_OUT | TIM15_CH2 | I2S1_SDO/SPI1_MOSI | SAI1_FS_B | USART2_RX | / | / | / | ETH2_COL | ETH2_COL | / | / |
| 79 | I2S1_CK | PC3 | / | / | SAI1_CK1 | DFSDM1_CKOUT | / | I2S1_SDI/SPI1_MISO | I2S1_CK/SPI1_SCK | / | UART5_CTS | / | SAI1_MCLK_A | ETH1_TX_CLK | ETH2_TX_CLK | / | / |
| 80 | I2S1_WS | PA4 | / | / | TIM5_ETR | USART2_CK | SAI1_SCK_B | I2S1_WS/SPI1_NSS | DFSDM1_CKIN1 | / | / | / | ETH1_PPS_OUT | ETH2_PPS_OUT | SAI1_SCK_A | / | / |
| 81 | SYSCLK | PF14 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 82 | SYSD10 | PF15 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 83 | BOOT2 | P16 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 84 | BOOT1 | P15 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 85 | BOOT0 | P14 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 86 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 87 | PT1 | P11 | / | / | / | / | / | / | / | / | SPDIFRX_IN1 | / | / | / | / | / | / |
| 88 | NRST | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 89 | PWM2 | PA11 | / | TIM1_CH4 | / | / | I2C5_SCL | I2S2_WS/SPI2_NSS | / | USART1_CTS/USART1_NSS | / | / | ETH2_RXD1 | ETH1_CLK | / | ETH2_CLK | / |
| 90 | PWM3 | PA10 | / | TIM1_CH3 | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 91 | ADC1_INP8 | PF11 | / | USART2_TX | SAI1_D2 | DFSDM1_CKIN3 | / | / | SAI1_FS_A | / | / | / | / | / | ETH2_RX_ER | / | / |
| 92 | ADC1_INP2 | PA5 | / | TIM2_CH1/TIM2_ETR | USART2_CK | TIM8_CH1N | SAI1_D1 | I2S1_WS/SPI1_NSS | SAI1_SD_A | / | / | / | ETH1_PPS_OUT | ETH2_PPS_OUT | / | / | / |
| 93 | I2C3_SCL | PH3 | / | / | / | / | I2C3_SCL | SPI5_MOSI | / | / | / | QUADSPI_BK2_IO1 | ETH1_COL | LTDC_R5 | ETH2_COL | QUADSPI_BK1_IO0 | LTDC_B4 |
| 94 | I2C3_SDA | PH7 | / | / | SAI2_FS_B | / | / | I2C3_SDA | SPI5_SCK | / | / | QUADSPI_BK2_IO3 | ETH2_TX_CLK | ETH1_TX_CLK | / | QUADSPI_BK1_IO3 | LTDC_B2 |
| 95 | FDCAN2_TX | PB13 | DEBUG_TRACECLK | TIM1_CH1N | / | / | LPTIM2_OUT | I2S2_WS/SPI2_NSS | I2C4_SCL | / | SDMMC1_D123DIR | FDCAN2_TX | / | UART5_TX | / | LTDC_CLK | / |
| 96 | FDCAN2_RX | PB5 | DEBUG_TRACED4 | TIM17_BKIN | TIM3_CH2 | / | / | I2S2_SDI/SPI2_MISO | I2C4_SMBA | / | SDMMC1_CKIN | FDCAN2_RX | / | UART5_RX | / | LTDC_B6 | LTDC_DE |
| 97 | PT7 | PT7 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 98 | PH4 | PB4 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 99 | PH5 | PB5 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 100 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 101 | PB10 | PB10 | / | TIM2_CH3 | / | LPTIM2_IN1 | I2C5_SMBA | I2S4_WS/SPI4_NSS | I2S2_CK/SPI2_SCK | / | / | / | / | / | / | / | LTDC_R3 |
| 102 | PT13 | PT13 | / | / | / | / | / | / | / | / | SPDIFRX_IN3 | / | / | ETH1_RX_ER | / | / | / |
| 103 | PT12 | PT12 | / | / | / | / | / | / | / | / | SPDIFRX_IN2 | / | / | / | / | / | / |
| 104 | PC13 | PC13 | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 105 | PE10 | PE10 | / | TIM1_CH2N | / | / | / | / | / | UART7_RX | / | FDCAN1_TX | / | / | FMC_D7/FMC_DA7 | / | / |
| 106 | PE8 | PE8 | / | TIM1_CH1N | / | DFSDM1_CKIN2 | / | I2C1_SDA | / | UART7_TX | / | / | / | / | FMC_D5/FMC_DA5 | / | / |
| 107 | PT0 | PT0 | / | / | / | / | / | / | / | / | SPDIFRX_IN0 | / | / | / | / | / | / |
| 108 | PA13 | PA13 | DEBUG_DBTRGO | DEBUG_DBTRGI | RCC_MCO_1 | / | / | / | / | / | UART4_TX | / | / | / | / | / | / |
| 109 | PG12 | PG12 | / | LPTIM1_IN1 | / | / | SAI2_SCK_A | / | SAI2_CK2 | USART6_DE/USART6_RTS | USART3_CTS | / | ETH2_PHY_INTN | ETH1_PHY_INTN | ETH2_CRS_DV/ETH2_RX_CTL/ETH2_RX_DV | / | / |
| 110 | PD12 | PD12 | / | LPTIM1_IN1 | TIM4_CH1 | / | / | I2C1_SCL | / | USART3_DE/USART3_RTS | / | / | / | / | FMC_A17/FMC_ALE | DCMIPP_D6 | / |
| 111 | NC | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 112 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 113 | SDMMC1_D0 | PC8 | DEBUG_TRACED0 | / | TIM3_CH3 | TIM8_CH3 | / | I2S3_SDI/SPI3_MISO | / | USART6_CK | USART3_CTS | / | SAI2_FS_B | UART5_DE/UART5_RTS | SDMMC1_D0 | / | LTDC_G7 |
| 114 | SDMMC1_CMD | PD2 | DEBUG_TRACED4 | / | TIM3_ETR | / | I2C1_SMBA | I2S3_WS/SPI3_NSS | SAI2_D1 | USART3_RX | / | / | / | / | SDMMC1_CMD | / | / |
| 115 | SDMMC1_D1 | PC9 | DEBUG_TRACED1 | / | TIM3_CH4 | TIM8_CH4 | / | / | / | USART3_RTS | UART5_CTS | FDCAN1_TX | / | / | SDMMC1_D1 | / | LTDC_B4 |
| 116 | SDMMC1_D3 | PC11 | DEBUG_TRACED3 | / | / | / | I2C1_SDA | / | I2S3_SDO/SPI3_MOSI | USART3_CK | UART5_RX | / | SAI2_SCK_B | / | SDMMC1_D3 | / | / |
| 117 | SDMMC1_D2 | PC10 | DEBUG_TRACED2 | / | / | / | / | I2C1_SCL | I2S3_CK/SPI3_SCK | USART3_TX | / | / | SAI2_MCLK_B | / | SDMMC1_D2 | / | / |
| 118 | SDMMC1_CK | PC12 | DEBUG_TRACECLK | / | / | / | / | / | / | / | UART7_TX | / | SAI2_SD_B | / | SDMMC1_CK | / | LTDC_DE |
| 119 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |
| 120 | LTDC_CLK | PB9 | DEBUG_TRACECLK | / | / | DFSDM1_DATIN3 | / | / | / | / | / | / | SDMMC2_CDIR | LTDC_B5 | FMC_D14/FMC_DA14 | LTDC_CLK | LTDC_B0 |
| 121 | LTDC_DE | PB9 | / | TIM1_CH4 | TIM12_CH2 | / | / | I2S4_CK/SPI4_SCK | DCMIPP_D13 | / | / | LTDC_B5 | / | LTDC_DE | FMC_A2/ | DCMIPP_D9 | DCMIPP_D8 |
| 122 | LTDC_HS | PE1 | / | LPTIM1_IN2 | / | / | / | / | / | / | UART8_TX | LTDC_HSYNC | / | LTDC_R4 | FMC_NBL1 | DCMIPP_D3 | DCMIPP_D12 |
| 123 | LTDC_VS | PG4 | DEBUG_TRACED1 | TIM1_BKIN2 | / | / | DFSDM1_CKIN3 | / | / | / | USART3_RX | / | SDMMC2_D123DIR | LTDC_VSYNC | FMC_A14 | DCMIPP_D8 | DCMIPP_D13 |
| 124 | LTDC_R2 | PB10 | DEBUG_TRACED0 | / | TIM5_CH1 | SAI2_D3 | DFSDM1_DATIN2 | I2S3_MCK | I2S2_SDO/SPI2_MOSI | USART3_CTS/USART3_NSS | / | / | / | / | / | LTDC_HSYNC | LTDC_R2 |
| 125 | LTDC_R3 | PB12 | DEBUG_TRACED10 | / | / | / | I2C2_SMBA | / | DFSDM1_DATIN1 | UART7_DE/UART7_RTS | / | / | / | UART5_RX | / | / | LTDC_R3 | LTDC_VSYNC |
| 126 | LTDC_R7 | PD11 | / | / | / | LPTIM2_IN2 | I2C4_SMBA | / | / | USART3_CTS/USART3_NSS | SPDIFRX_IN0 | QUADSPI_BK1_IO2 | ETH2_CLK125 | LTDC_R7 | FMC_A16/FMC_CLE | UART7_RX | DCMIPP_D4 |
| 127 | LTDC_R0 | PE11 | / | TIM1_CH2 | RT2_CTS/USART2_ | / | SAI1_D2 | I2S4_SDO/SPI4_MOSI | SAI1_FS_A | USART6_CK | / | LTDC_R0 | ETH2_TX_ER | ETH1_TX_ER | FMC_D8/FMC_DA8 | DCMIPP_D10 | LTDC_R5 |
| 128 | LTDC_R1 | PG7 | DEBUG_TRACED8 | TIM1_ETR | / | / | / | I2S3_SDI/SPI3_MISO | / | / | UART7_CTS | / | SDMMC2_CKIN | LTDC_R1 | / | LTDC_R5 | LTDC_R2 |
| 129 | LTDC_R6 | PD4 | / | / | / | USART2_DE/USART2_RTS | / | I2S3_SDI/SPI3_MISO | DFSDM1_CKIN0 | / | / | QUADSPI_CLK | / | LTDC_R1 | FMC_NOE | LTDC_R4 | LTDC_R6 |
| 130 | LTDC_R4 | PD14 | / | / | TIM4_CH3 | / | I2C3_SDA | / | / | USART1_RX | UART8_CTS | / | / | / | FMC_D0/FMC_DA0 | DCMIPP_D8 | LTDC_R4 |
| 131 | LTDC_R5 | PF5 | DEBUG_TRACED12 | / | / | / | DFSDM1_CKIN0 | I2C1_SMBA | / | / | / | LTDC_G0 | / | / | FMC_A5 | DCMIPP_D11 | LTDC_R5 |
| 132 | LTDC_G3 | PF3 | / | / | / | LPTIM2_IN2 | I2C5_SDA | I2S4_SDI/SPI4_MISO | I2S3_WS/SPI3_NSS | / | / | / | / | / | FMC_A3 | / | LTDC_G3 |
| 133 | LTDC_G2 | PH13 | DEBUG_TRACED15 | / | USART2_CK | TIM8_CH1N | I2C5_SCL | / | I2S3_CK/SPI3_SCK | / | UART4_TX | / | / | / | / | LTDC_G3 | LTDC_G2 |
| 134 | LTDC_G4 | PF2 | DEBUG_TRACED1 | / | / | / | I2C2_SCL | / | DFSDM1_CKIN1 | USART6_CK | / | SDMMC2_D/DIR | / | SDMMC1_D/DIR | FMC_A2 | LTDC_G4 | LTDC_B3 |
| 135 | LTDC_G1 | PF1 | DEBUG_TRACED7 | / | / | / | I2C2_SDA | I2S3_SDO/SPI3_MOSI | / | / | / | / | / | / | FMC_A1 | LTDC_B7 | LTDC_G1 |
| 136 | LTDC_G5 | PG0 | / | / | / | / | / | / | / | / | / | FDCAN2_TX | / | / | FMC_A10 | DCMIPP_PIXCLK | LTDC_G5 |
| 137 | LTDC_G7 | PA15 | DEBUG_TRACED5 | TIM2_CH1 | / | / | / | I2S4_MCK | / | UART4_DE/UART4_RTS | UART4_RX | LTDC_R0 | / | LTDC_G7 | FMC_A9 | DCMIPP_D14 | DCMIPP_D5 |
| 138 | LTDC_G6 | PE12 | / | TIM1_CH3N | / | / | / | I2S4_CK/SPI4_SCK | / | / | UART8_DE/UART8_RTS | LTDC_VSYNC | / | LTDC_G4 | FMC_D9/FMC_DA9 | DCMIPP_D11 | LTDC_G6 |
| 139 | LTDC_G0 | PE14 | / | TIM1_BKIN | / | / | SAI1_D4 | / | / | / | UART8_DE/UART8_RTS | QUADSPI_BK1_NCS | QUADSPI_BK2_IO2 | / | FMC_D11/FMC_DA11 | DCMIPP_D7 | LTDC_G0 |
| 140 | LTDC_B0 | PD6 | / | / | / | / | / | / | / | / | / | QUADSPI_BK1_IO0 | / | / | FMC_NWE | LTDC_B0 | LTDC_G4 |
| 141 | LTDC_B7 | PE15 | / | TIM2_ETR | TIM4_BKIN | USART2_CTS/USART2_NSS | / | / | I2C4_SCL | / | / | / | / | / | FMC_D12/FMC_DA12 | DCMIPP_D10 | LTDC_B7 |
| 142 | LTDC_B3 | PE7 | / | TIM1_ETR | / | / | LPTIM2_IN1 | / | / | / | UART5_TX | / | / | / | FMC_D4/FMC_DA4 | LTDC_B3 | LTDC_B5 |
| 143 | LTDC_B2 | PD10 | RTC_REFIN | / | / | / | I2C5_SMBA | I2S4_WS/SPI4_NSS | / | USART3_CK | / | LTDC_G5 | / | LTDC_B7 | FMC_D15/FMC_DA15 | DCMIPP_VSYNC | LTDC_B2 |
| 144 | LTDC_B5 | PD15 | / | USART2_RX | TIM4_CH4 | DFSDM1_DATIN2 | / | / | / | / | / | QUADSPI_BK1_IO3 | / | / | FMC_D1/FMC_DA1 | / | LTDC_B5 |
| 145 | LTDC_B4 | PG15 | / | / | / | / | / | / | / | USART6_CTS/USART6_NSS | UART7_CTS | QUADSPI_BK1_IO1 | ETH2_PHY_INTN | LTDC_B4 | / | DCMIPP_D10 | LTDC_B3 |
| 146 | LTDC_B1 | PB0 | / | / | / | / | / | / | DCMIPP_D12 | / | UART8_RX | FDCAN2_RX | / | LTDC_B1 | FMC_A11 | DCMIPP_D1 | LTDC_B5 |
| 147 | LTDC_B6 | PD1 | / | / | / | / | I2C5_SCL | I2S4_SDO/SPI4_MOSI | / | / | UART8_TX | QUADSPI_BK1_NCS | / | LTDC_B6 | FMC_D3/FMC_DA3 | DCMIPP_D13 | LTDC_G2 |
| 148 | GND | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / | / |