

MYC-YT113X Pin List V1.0

Pin No	Pin Name	Pin Is GPIO	T113-S3 Pin Name	Default function	IO Voltage	Description
1	GND	/	/	/	0V	
2	PD14	PD14	PD14/LCD0_D20/LVDS1_V2P/SPI1_HOLD/DBI_DCX/DBI_WRX/UART3_CTS/PD_EINT14	GPIO	0/3.3V	
3	PD15	PD15	PD15/LCD0_D21/LVDS1_V2N/SPI1_WP/DBI_TE/IR_RX/PD_EINT15	GPIO	0/3.3V	
4	GND	/	/	/	0V	
5	PD16	PD16	PD16/LCD0_D22/LVDS1_CKP/DMIC_DATA3/PWM0/PD_EINT16	GPIO	0/3.3V	
6	PD17	PD17	PD17/LCD0_D23/LVDS1_CKN/DMIC_DATA2/PWM1/PD_EINT17	GPIO	0/3.3V	
7	GND	/	/	/	0V	
8	PD18	PD18	PD18/LCD0_CLK/LVDS1_V3P/DMIC_DATA1/PWM2/PD_EINT18	GPIO	0/3.3V	
9	PD19	PD19	PD19/LCD0_DE/LVDS1_V3N/DMIC_DATA0/PWM3/PD_EINT19	GPIO	0/3.3V	
10	GND	/	/	/	0V	
11	TWI3_SCK	PB6	PB6/LCD0_D16/I2S2_LRCK/TWI3_SCK/PWM1/LCD0_D22/UART3_TX/CPUBIST0/PB_EINT6	I2C3	0/3.3V	
12	TWI3_SDA	PB7	PB7/LCD0_D17/I2S2_MCLK/TWI3_SDA/IR_RX/LCD0_D23/UART3_RX/CPUBIST1/PB_EINT7	I2C3	0/3.3V	
13	TWI1_SDA	PB5	PB5/LCD0_D9/I2S2_BCLK/TWI1_SDA/PWM0/LCD0_D21/UART5_RX/CAN1-RX0/PB_EINT5	I2C1	0/3.3V	
14	GND	/	/	/	0V	
15	CAN0-TX	PB2	PB2/LCD0_D0/I2S2_DOUT2/TWI0_SDA/I2S2_DIN2/LCD0_D18/UART4_TX/CAN0-TX0/PB_EINT2	CAN	0/3.3V	
16	CAN0-RX	PB3	PB3/LCD0_D1/I2S2_DOUT1/TWI0_SCK/I2S2_DIN0/LCD0_D19/UART4_RX/CAN0-RX0/PB_EINT3	CAN	0/3.3V	
17	TWI1_SCK	PB4	PB4/LCD0_D8/I2S2_DOUT0/TWI1_SCK/I2S2_DIN1/LCD0_D20/UART5_TX/CAN1-TX0/PB_EINT4	I2C1	0/3.3V	
18	AGND	/	/	/	0V	
19	MICN	/	MICIN3N	MICIN3N	0~3.3V	
20	MICP	/	MICIN3P	MICIN3P	0~3.3V	
21	AGND	/	/	/	0V	
22	FMINR	/	FMINR	FMINR	0~3.3V	
23	FMINL	/	FMINL	FMINL	0~3.3V	
24	AGND	/	/	/	0V	
25	LINEINR	/	LINEINR	LINEINR	0~3.3V	
26	LINEINL	/	LINEINL	LINEINL	0~3.3V	
27	AGND	/	/	/	0V	
28	NC	/	/	/	/	
29	HPOUTR	/	HPOUTR	HPOUTR	0~3.3V	
30	HPOUTL	/	HPOUTL	HPOUTL	0~3.3V	
31	HPOUTFB	/	HPOUTFB	HPOUTFB	0~3.3V	

32	AGND	/	/	/	0V	
33	TV-IN0	/	TV-IN0	TV-IN0	1.8V	
34	TV-IN1	/	TV-IN1	TV-IN1	1.8V	
35	AGND	/	/	/	0V	
36	GPADC0	/	GPADC0	GPADC0	0V~1.8V	
37	TP-X1	/	TP-X1	TP-X1	0V~1.8V	
38	TP-X2	/	TP-X2	TP-X2	0V~1.8V	
39	TP-Y1	/	TP-Y1	TP-Y1	0V~1.8V	
40	TP-Y2	/	TP-Y2	TP-Y2	0V~1.8V	
41	AGND	/	/	/	0V	
42	TV-OUT	/	TV-OUT	TV-OUT	0~3.3V	
43	AGND	/	/	/	0V	
44	NC	/	/	/	/	
45	CARRIER_PWR_EN	/	/	/	0/3.3V	Output high level to enable carried board voltage
46	PD20	PD20	PD20/LCD0_HSYNC/TWI2_SCK/DMIC_CLK/PWM4/PD_EINT20	GPIO	0/3.3V	
47	PD21	PD21	PD21/LCD0_VSYNC/TWI2_SDA/UART1_TX/PWM5/PD_EINT21	GPIO	0/3.3V	
48	PD22	PD22	PD22/SPDIF_OUT/IR_RX/UART1_RX/PWM7/PD_EINT22	GPIO	0/3.3V	
49	NC	/	/	/	/	
50	NC	/	/	/	/	
51	GND	/	/	/	0V	
52	PG11	PG11	PG11/I2S1_MCLK/TWI3_SDA/EPHY_25M/CLK_FANOUT1/TCON_TRIG/PG_EINT11	GPIO	0/3.3V	
53	PG13	PG13	PG13/I2S1_BCLK/TWI0_SDA/RGMII_CLKIN/RMII_RXER/PWM2/LEDC_DO/UART1_RX/PG_EINT13	GPIO	0/3.3V	
54	SDC0-D0	/	PF1/SDC0_D0/JTAG_DI/R_JTAG_DI/I2S2_DOUT0/I2S2_DIN1/PF_EINT1	SMHC	0/3.3V	
55	SDC0-D1	/	PF0/SDC0_D1/JTAG_MS/R_JTAG_MS/I2S2_DOUT1/I2S2_DIN0/PF_EINT0	SMHC	0/3.3V	
56	SDC0-CMD	/	PF3/SDC0_CMD/JTAG_DO/R_JTAG_DO/I2S2_BCLK/PF_EINT3	SMHC	0/3.3V	
57	SDC0-D3	/	PF4/SDC0_D3/UART0_RX/TWI0_SDA/PWM6/IR_TX/PF_EINT4	SMHC	0/3.3V	
58	SDC0-D2	/	PF5/SDC0_D2/JTAG_CK/R_JTAG_CK/I2S2_LRCK/PF_EINT5	SMHC	0/3.3V	
59	SDC0-DET	/	PF6/SPDIF_OUT/IR_RX/I2S2_MCLK/PWM5/PF_EINT6	SMHC	0/3.3V	
60	GND	/	/	/	0V	
61	SDC0-CLK	/	PF2/SDC0_CLK/UART0_TX/TWI0_SCK/LEDC_DO/SPDIF_IN/PF_EINT2	SMHC	0/3.3V	
62	GND	/	/	/	0V	
63	VDD_SOM_3V3OUT	/	/	/	3.3V	3.3V Output
64	NC	/	/	/	/	
65	GND	/	/	/	0V	

66	GND	/	/	/	0V	
67	GND	/	/	/	0V	
68	VDD_5V	/	/	/	5V	5V DC In
69	VDD_5V	/	/	/	5V	5V DC In
70	VDD_5V	/	/	/	5V	5V DC In
71	CLK24M_OUT	/	CLK24M_OUT	CLK24M_OUT	0/3.3V	
72	SYS-RST-OUT	/	SYS-RST-OUT	SYS-RST-OUT	0/3.3V	
73	SYS-RST-IN	/	/	/	0/3.3V	Hardware system reset
74	WDI	/	/	/	0/3.3V	
75	GND	/	/	/	0V	
76	PG3	PG3	PG3/SDC1_D1/UART3_CTS/RGMII_TXCK/RMII_TXCK/UART4_RX/PG_EINT3	RGMII-TXCK	0/3.3V	
77	GND	/	/	/	0V	
78	PG14	PG14	PG14/I2S1_DIN0/TWI2_SCK/MDC/I2S1_DOUT1/SPI0_WP/UART1_RTS/PG_EINT14	RGMII-MDC	0/3.3V	
79	PG15	PG15	PG15/I2S1_DOUT0/TWI2_SDA/MDIO/I2S1_DIN1/SPI0_HOLD/UART1_CTS/PG_EINT15	RGMII-MDIO	0/3.3V	
80	PG4	PG4	PG4/SDC1_D2/UART5_TX/RGMII_TXD0/RMII_TXD0/PWM5/PG_EINT4	RGMII-TXD0	0/3.3V	
81	PG5	PG5	PG5/SDC1_D3/UART5_RX/RGMII_TXD1/RMII_TXD1/PWM4/PG_EINT5	RGMII-TXD1	0/3.3V	
82	PG12	PG12	PG12/I2S1_LRCK/TWI0_SCK/RGMII_TXCTRL/RMII_TXEN/CLK_FANOUT2/PWM0/UART1_TX/PG_EINT12	RGMII-TXCTL	0/3.3V	
83	PG7	PG7	PG7/UART1_RX/TWI2_SDA/RGMII_TXD3/SPDIF_IN/PG_EINT7	RGMII-TXD3	0/3.3V	
84	PG6	PG6	PG6/UART1_TX/TWI2_SCK/RGMII_TXD2/PWM1/PG_EINT6	RGMII-TXD2	0/3.3V	
85	GND	/	/	/	0V	
86	PG9	PG9	PG9/UART1_CTS/TWI1_SDA/RGMII_RXD3/UART3_RX/PG_EINT9	RGMII-RXD3	0/3.3V	
87	PG8	PG8	PG8/UART1_RTS/TWI1_SCK/RGMII_RXD2/UART3_TX/PG_EINT8	RGMII-RXD2	0/3.3V	
88	PG10	PG10	PG10/PWM3/TWI3_SCK/RGMII_RXCK/CLK_FANOUT0/IR_RX/PG_EINT10	RGMII-RXCK	0/3.3V	
89	GND	/	/	/	0V	
90	PG0	PG0	PG0/SDC1_CLK/UART3_TX/RGMII_RXCTRL/RMII_CRS_DV/PWM7/PG_EINT0	RGMII-RXCTL	0/3.3V	
91	PG2	PG2	PG2/SDC1_D0/UART3_RTS/RGMII_RXD1/RMII_RXD1/UART4_TX/PG_EINT2	RGMII-RXD1	0/3.3V	
92	PG1	PG1	PG1/SDC1_CMD/UART3_RX/RGMII_RXD0/RMII_RXD0/PWM6/PG_EINT1	RGMII-RXD0	0/3.3V	
93	GND	/	/	/	0V	
94	PE10	PE10	PE10/NCSI0_D6/UART1_TX/PWM4/IR_RX/JTAG_DO/EPHY_25M/PE_EINT10	GPIO	0/3.3V	
95	PE11	PE11	PE11/NCSI0_D7/UART1_RX/I2S0_DOUT3/I2S0_DIN3/JTAG_CK/RGMII_TXD2/PE_EINT11	GPIO	0/3.3V	
96	PWM5	PE13	PE13/TWI2_SDA/PWM5/I2S0_DOUT0/I2S0_DIN1/DMIC_DATA3/RGMII_RXD2/PE_EINT13	PWM	0/3.3V	
97	GND	/	/	/	0V	
98	PE2	PE2	PE2/NCSI0_PCLK/UART2_TX/TWI0_SCK/CLK_FANOUT0/UART0_TX/RGMII_RXD1/RMII_RXD1/PE_EINT2	GPIO	0/3.3V	
99	PE12	PE12	PE12/TWI2_SCK/NCSI0_FIELD/I2S0_DOUT2/I2S0_DIN2/RGMII_TXD3/PE_EINT12	GPIO	0/3.3V	
100	NC	/	/	/	3.3V	

101	PWM2	PE8	PE8/NCSI0_D4/UART1_RTS/PWM2/UART3_TX/JTAG_MS/MDC/PE_EINT8	PWM	0/3.3V	
102	PWM3	PE9	PE9/NCSI0_D5/UART1_CTS/PWM3/UART3_RX/JTAG_DI/MDIO/PE_EINT9	PWM	0/3.3V	
103	NC	/	/	/	/	
104	GND	/	/	/	0V	
105	UART5_TX	PE6	PE6/NCSI0_D2/UART5_TX/TWI3_SCK/SPDIF_IN/D_JTAG_DO/R_JTAG_DO/RGMII_TXCTRL/RMII_TXEN/PE_EINT6	UART5	0/3.3V	
106	UART5_RX	PE7	PE7/NCSI0_D3/UART5_RX/TWI3_SDA/SPDIF_OUT/D_JTAG_CK/R_JTAG_CK/RGMII_CLKIN/RMII_RXER/PE_EINT7	UART5	0/3.3V	
107	UART4_TX	PE4	PE4/NCSI0_D0/UART4_TX/TWI2_SCK/CLK_FANOUT2/D_JTAG_MS/R_JTAG_MS/RGMII_TXD0/RMII_TXD0/PE_EINT4	UART4	0/3.3V	
108	PE3	PE3	PE3/NCSI0_MCLK/UART2_RX/TWI0_SDA/CLK_FANOUT1/UART0_RX/RGMII_TXCK/RMII_TXCK/PE_EINT3	GPIO	0/3.3V	
109	UART4_RX	PE5	PE5/NCSI0_D1/UART4_RX/TWI2_SDA/LEDC_DO/D_JTAG_DI/R_JTAG_DI/RGMII_TXD1/RMII_TXD1/PE_EINT5	UART4	0/3.3V	
110	PE1	PE1	PE1/NCSI0_VSYNC/UART2_CTS/TWI1_SDA/LCD0_VSYNC/RGMII_RXD0/RMII_RXD0/PE_EINT1	GPIO	0/3.3V	
111	PE0	PE0	PE0/NCSI0_HSYNC/UART2_RTS/TWI1_SCK/LCD0_HSYNC/RGMII_RXCTRL/RMII_CRS_DV/PE_EINT0	GPIO	0/3.3V	
112	GND	/	/	/	0V	
113	USB0-DP	/	USB0-DP	USB0-DP	/	
114	USB0-DM	/	USB0-DM	USB0-DM	/	
115	GND	/	/	/	0V	
116	USB1-DP	/	USB1-DP	USB1-DP	0V	
117	USB1-DM	/	USB1-DM	USB1-DM	/	
118	GND	/	/	/	0V	
119	GND	/	/	/	0V	
120	PD1	PD1	PD1/LCD0_D3/LVDS0_V0N/DSI_D0N/UART2_TX/PD_EINT1	GPIO	0/3.3V	
121	PD0	PD0	PD0/LCD0_D2/LVDS0_V0P/DSI_D0P/TWI0_SCK/PD_EINT0	GPIO	0/3.3V	
122	GND	/	/	/	0V	
123	PD3	PD3	PD3/LCD0_D5/LVDS0_V1N/DSI_D1N/UART2_RTS/PD_EINT3	GPIO	0/3.3V	
124	PD2	PD2	PD2/LCD0_D4/LVDS0_V1P/DSI_D1P/UART2_RX/PD_EINT2	GPIO	0/3.3V	
125	GND	/	/	/	0V	
126	PD5	PD5	PD5/LCD0_D7/LVDS0_V2N/DSI_CKN/UART5_TX/PD_EINT5	GPIO	0/3.3V	
127	PD4	PD4	PD4/LCD0_D6/LVDS0_V2P/DSI_CKP/UART2_CTS/PD_EINT4	GPIO	0/3.3V	
128	GND	/	/	/	0V	
129	PD7	PD7	PD7/LCD0_D11/LVDS0_CKN/DSI_D2N/UART4_TX/PD_EINT7	GPIO	0/3.3V	
130	PD6	PD6	PD6/LCD0_D10/LVDS0_CKP/DSI_D2P/UART5_RX/PD_EINT6	GPIO	0/3.3V	
131	GND	/	/	/	0V	
132	PD9	PD9	PD9/LCD0_D13/LVDS0_V3N/DSI_D3N/PWM6/PD_EINT9	GPIO	0/3.3V	
133	PD8	PD8	PD8/LCD0_D12/LVDS0_V3P/DSI_D3P/UART4_RX/PD_EINT8	GPIO	0/3.3V	
134	GND	/	/	/	0V	
135	PD11	PD11	PD11/LCD0_D15/LVDS1_V0N/SPI1_CLK/DBI_SCLK/UART3_RX/PD_EINT11	GPIO	0/3.3V	
136	PD10	PD10	PD10/LCD0_D14/LVDS1_V0P/SPI1_CS/DBI_CSX/UART3_TX/PD_EINT10	GPIO	0/3.3V	

137	GND	/	/	/	0V	
138	PD13	PD13	PD13/LCD0_D19/LVDS1_V1N/SPI1_MISO/DBI_SDI/DBI_TE/DBI_DCX/UART3_RTS/PD_EINT13	GPIO	0/3.3V	
139	PD12	PD12	PD12/LCD0_D18/LVDS1_V1P/SPI1_MOSI/DBI_SDO/TWI0_SDA/PD_EINT12	GPIO	0/3.3V	
140	GND	/	/	/	0V	

END