

Function	SoC Pin Name	Bank	Ball	Net Name	J1	Net Name	Ball	Bank	SoC Pin Name	Function
DC 5V input				VDD_5V	1 2	VDD_5V				DC 5V input
DC 5V input				VDD_5V	3 4	VDD_5V				DC 5V input
				GND	5 6	GND				Ground
Ethernet Data 0 Positive				RGMIIO_TRP0	7 8	GND				Ground
Ethernet Data 0 Negative				RGMIIO_TRN0	9 10	RGMIIO_TRP1				Ethernet Data 1 Positive
				GND	11 12	RGMIIO_TRN1				Ethernet Data 1 Negative
Ethernet Data 2 Positive				RGMIIO_TRP2	13 14	GND				Ground
Ethernet Data 2 Negative				RGMIIO_TRN2	15 16	RGMIIO_TRP3				Ethernet Data 3 Positive
				GND	17 18	RGMIIO_TRN3				Ethernet Data 3 Negative
USB Data plus				USB_PHY_DP	19 20	GND				Ground
USB Data minus				USB_PHY_DM	21 22	MDIO_CK	C10		PS_MIO52_501	MDIO Clk
				GND	23 24	MDIO_DATA	C11		PS_MIO53_501	MDIO Data
USB OTG ID				USB_PHY_ID	25 26	RGMIIO_LED_ACT				Ethernet Status LED
USB VBUS				USB_PHY_VBUS	27 28	RGMIIO_LED_LINK				Ethernet Link LED
FMC	IO_0_35	Bank 35	G14	IO_B35_0	29 30	VBUS_SW_EN				USB Active high VBUS control output
FMC	IO_L7P_T1_AD2P_35	Bank 35	M19	IO_B35_LP7	31 32	GND				
FMC	IO_L7N_T1_AD2N_35	Bank 35	M20	IO_B35_LP7	33 34	IO_B35_LP18	G19	Bank 35	IO_L18P_T2_AD13P_35	FMC
FMC	IO_L21P_T3_DQ5_AD14P_35	Bank 35	N15	IO_B35_LP21	35 36	IO_B35_LN18	G20	Bank 35	IO_L18N_T2_AD13N_35	FMC
FMC	IO_L21N_T3_DQ5_AD14N_35	Bank 35	N16	IO_B35_LP21	37 38	IO_B35_LP9	L19	Bank 35	IO_L9P_T1_DQ5_AD3P_35	FMC
FMC	IO_L17P_T2_AD5P_35	Bank 35	J20	IO_B35_LP17	39 40	IO_B35_LN9	L20	Bank 35	IO_L9N_T1_DQ5_AD3N_35	FMC
FMC	IO_L17N_T2_AD5N_35	Bank 35	H20	IO_B35_LP17	41 42	IO_B35_LP11	L16	Bank 35	IO_L11P_T1_SRC3_35	FMC
				GND	43 44	IO_B35_LN11	L17	Bank 35	IO_L11N_T1_SRC3_35	FMC
FMC	IO_L1P_T0_AD0P_35	Bank 35	C20	IO_B35_LP1	45 46	GND				
FMC	IO_L1N_T0_AD0N_35	Bank 35	B20	IO_B35_LN1	47 48	IO_B35_LP4	D19	Bank 35	IO_L4P_T0_35	FMC
FMC	IO_L15P_T2_DQ5_AD12P_35	Bank 35	F19	IO_B35_LP15	49 50	IO_B35_LN4	D20	Bank 35	IO_L4N_T0_35	FMC
FMC	IO_L15N_T2_DQ5_AD12N_35	Bank 35	F20	IO_B35_LP15	51 52	IO_B35_LP8	M17	Bank 35	IO_L8P_T1_AD10P_35	FMC
				GND	53 54	IO_B35_LN8	M18	Bank 35	IO_L8N_T1_AD10N_35	FMC
FMC	IO_L12P_T1_MRCC_35	Bank 35	K17	IO_B35_LP12	55 56	IO_B35_LP16	G17	Bank 35	IO_L16P_T2_35	FMC
FMC	IO_L12N_T1_MRCC_35	Bank 35	K18	IO_B35_LP12	57 58	IO_B35_LN16	G18	Bank 35	IO_L16N_T2_35	FMC
Power-supply pins for the output drivers of Bank 35				VDDIO_35_PL	59 60	VDDIO_35_PL				Power-supply pins for the output drivers of Bank 35
				GND	61 62	GND				
FMC	IO_L5P_T0_AD9P_35	Bank 35	E18	IO_B35_LP5	63 64	IO_B35_LP10	K19	Bank 35	IO_L10P_T1_AD11P_35	FMC
FMC	IO_L5N_T0_AD9N_35	Bank 35	E19	IO_B35_LP5	65 66	IO_B35_LN10	J19	Bank 35	IO_L10N_T1_AD11N_35	FMC
FMC	IO_L3P_T0_DQ5_AD1P_35	Bank 35	E17	IO_B35_LP3	67 68	IO_B35_LP14	J18	Bank 35	IO_L14P_T2_AD4P_SRC3_35	FMC
FMC	IO_L3N_T0_DQ5_AD1N_35	Bank 35	D18	IO_B35_LP3	69 70	IO_B35_LN14	H18	Bank 35	IO_L14N_T2_AD4N_SRC3_35	FMC
FMC	IO_L2P_T0_AD8P_35	Bank 35	B19	IO_B35_LP2	71 72	IO_B35_LP6	F16	Bank 35	IO_L6P_T0_35	FMC
FMC	IO_L2N_T0_AD8N_35	Bank 35	A20	IO_B35_LP2	73 74	IO_B35_LN6	F17	Bank 35	IO_L6N_T0_VREF_35	FMC
				GND	75 76	GND				
FMC	IO_L13P_T2_MRCC_35	Bank 35	H16	IO_B35_LP13	77 78	IO_B35_LP24	K16	Bank 35	IO_L24P_T3_AD15P_35	FMC
FMC	IO_L13N_T2_MRCC_35	Bank 35	H17	IO_B35_LP13	79 80	IO_B35_LN24	J16	Bank 35	IO_L24N_T3_AD15N_35	FMC
				GND	81 82	IO_B35_LP20	K14	Bank 35	IO_L20P_T3_AD6P_35	FMC
FMC	IO_L19P_T3_35	Bank 35	H15	IO_B35_LP19	83 84	IO_B35_LN20	J14	Bank 35	IO_L20N_T3_AD6N_35	FMC
FMC	IO_L19N_T3_VREF_35	Bank 35	G15	IO_B35_LP19	85 86	IO_B35_LP22	L14	Bank 35	IO_L22P_T3_AD7P_35	FMC
FMC	IO_L23P_T3_35	Bank 35	M14	IO_B35_LP23	87 88	IO_B35_LN22	L15	Bank 35	IO_L22N_T3_AD7N_35	FMC
FMC	IO_L23N_T3_35	Bank 35	M15	IO_B35_LP23	89 90	IO_B35_25	J15	Bank 35	IO_25_35	FMC
				GND	91 92	GND				
XADC analog positive supply voltage	VCCADC_0	Bank 0	J9	XADC_VCC	93 94	JTAG_TCK	F9	Bank 0	TCK_0	JTAG Test Clock
XADC	VP_0		K9	XADC_INP0	95 96	JTAG_TMS	J6	Bank 0	TMS_0	JTAG Test Mode Select
XADC	VM_0		L10	XADC_INN0	97 98	JTAG_TDI	G6	Bank 0	TDI_0	JTAG Test Data Input
XADC	DXP_0		M9	XADC_TEMP_P	99 100	JTAG_TDO	F6	Bank 0	TDO_0	JTAG Test Data Output
XADC	DXN_0		M10	XADC_TEMP_N	101 102	GND				
XADC Ground				XADC_GND	103 104	SDIO0_CLK	D14	Bank 501	PS_MIO40_501	SDIO Clock
Ground				GND	105 106	SDIO0_CMD	C17	Bank 501	PS_MIO41_501	SDIO Command
PS_MIO0	PS_MIO0_500		E6	PS_MIO0	107 108	SDIO0_D1	E12	Bank 501	PS_MIO42_501	SDIO Data 0
JTAG Mode Select Pin				S_JTAG_H	109 110	SDIO0_D2	A9	Bank 501	PS_MIO43_501	SDIO Data 1
Boot Mode Select Pin				SD_BOOT_CONF_H	111 112	SDIO0_D3	F13	Bank 501	PS_MIO44_501	SDIO Data 2
CAN Transmit	PS_MIO8_500	Bank 500	D5	PS_MIO8	113 114	SDIO0_D4	B15	Bank 501	PS_MIO45_501	SDIO Data 3
CAN Receive	PS_MIO9_500	Bank 500	B5	PS_MIO9	115 116	SDIO0_CD	D16	Bank 501	PS_MIO46_501	SDIO Card Detection
eMMC Data 0	PS_MIO10_500	Bank 500	E9	PS_MIO10	117 118	SDIO0_WP	B14	Bank 501	PS_MIO47_501	SDIO Write Protected
eMMC Command	PS_MIO11_500	Bank 500	C6	PS_MIO11	119 120	GND				
eMMC Clock	PS_MIO12_500	Bank 500	D9	PS_MIO12	121 122	UART1_TX	B12	Bank 501	PS_MIO48_501	UART Transmit
eMMC Data 1	PS_MIO13_500	Bank 500	E8	PS_MIO13	123 124	UART1_RX	C12	Bank 501	PS_MIO49_501	UART Receive
eMMC Data 2	PS_MIO14_500	Bank 500	C5	PS_MIO14	125 126	PS_501_MIO_50	B13	Bank 501	PS_MIO50_501	I2C0_SCL
eMMC Data 3	PS_MIO15_500	Bank 500	C8	PS_MIO15	127 128	PS_501_MIO_51	B9	Bank 501	PS_MIO51_501	I2C0_SDA
				GND	129 130	FPGA_INITn	R10	Bank 0	INIT_B_0	Active Low, indicates initialization of configuration memory.
System Power Good				Sys_PG	131 132	FPGA_WARMRESETn	L6	Bank 0	PROGRAM_B_0	Active Low, asynchronous reset to configuration logic.
Active Low, PS Bank 500 Reset Output				PS_500_RESET_OUTn	133 134	FPGA_INIT_DONE	R11	Bank 0	DONE_0	Active High, DONE indicates successful completion of configuration.
Active Low, PS Warm Reset Input				PS_WARMRESET_Inn	135 136	VBAT_KEY_BACK_1.8V	F11	Bank 0	VCCBATT_0	PL decryption key memory backup
Active Low, eMMC Reset				eMMC_RSTn	137 138	VDDIO_501_PS				Power-supply pins for the output drivers of Bank 501
Power-supply pins for the output drivers of Bank 500				VDDIO_500_PS	139 140	GND				

Function	SoC Pin Name	Bank	Ball	Net Name	J2	Net Name	Ball	Bank	SoC Pin Name	Function
DC 5V input				VDD_5V	1 2	VDD_5V				DC 5V input
DC 5V input				VDD_5V	3 4	VDD_5V				DC 5V input
				GND	5 6	GND				Ground
LCD Display Enable	IO_25_34	Bank 34	T19	IO_B34_25	7 8	IO_B34_0	R19	Bank 34	IO_0_34	LCD Backlight Enable
LCD Data Enable	IO_L13P_T2_MRCC_34	Bank 34	N18	IO_B34_LP13	9 10	IO_B34_LP24	P15	Bank 34	IO_L24P_T3_34	Touchscreen Reset
LCD Peix Clock	IO_L13N_T2_MRCC_34	Bank 34	P19	IO_B34_LN13	11 12	IO_B34_LN24	P16	Bank 34	IO_L24N_T3_34	Touchscreen Interrupt
LCD Data line 14	IO_L22P_T3_34	Bank 34	W18	IO_B34_LP22	13 14	IO_B34_LP21	V17	Bank 34	IO_L21P_T3_DQ5_34	LCD vertical Synchronization signal
LCD Data line 15	IO_L22N_T3_34	Bank 34	W19	IO_B34_LN22	15 16	IO_B34_LN21	V18	Bank 34	IO_L21N_T3_DQ5_34	LCD horizontal Synchronization signal
LCD Data line 10	IO_L23P_T3_34	Bank 34	N17	IO_B34_LP23	17 18	IO_B34_LP20	T17	Bank 34	IO_L20P_T3_34	LCD Data line 12
LCD Data line 11	IO_L23N_T3_34	Bank 34	P18	IO_B34_LN23	19 20	IO_B34_LN20	R18	Bank 34	IO_L20N_T3_34	LCD Data line 13
				GND	21 22	GND				
LCD Data line 8	IO_L19P_T3_34	Bank 34	R16	IO_B34_LP19	23 24	IO_B34_LP18	V16	Bank 34	IO_L18P_T2_34	LCD Data line 6
LCD Data line 9	IO_L19N_T3_VREF_34	Bank 34	R17	IO_B34_LN19	25 26	IO_B34_LN18	W16	Bank 34	IO_L18N_T2_34	LCD Data line 7
LCD Data line 4	IO_L9P_T1_DQ5_34	Bank 34	T16	IO_B34_LP9	27 28	IO_B34_LP12	U18	Bank 34	IO_L12P_T1_MRCC_34	LCD Data line 2
LCD Data line 5	IO_L9N_T1_DQ5_34	Bank 34	U17	IO_B34_LN9	29 30	IO_B34_LN12	U19	Bank 34	IO_L12N_T1_MRCC_34	LCD Data line 3
LCD Data line 0	IO_L5P_T0_34	Bank 34	T14	IO_B34_LP5	31 32	IO_B34_LP14	N20	Bank 34	IO_L14P_T2_SRC3_34	FMC
LCD Data line 1	IO_L5N_T0_34	Bank 34	T15	IO_B34_LN5	33 34	IO_B34_LN14	P20	Bank 34	IO_L14N_T2_SRC3_34	FMC
Power-supply pins for the output drivers of Bank 34				VDDIO_34_PL	35 36	VDDIO_34_PL				Power-supply pins for the output drivers of Bank 34
				GND	37 38	GND				
FMC	IO_L11P_T1_SRC3_34	Bank 34	U14	IO_B34_LP11	39 40	IO_B34_LP15	T20	Bank 34	IO_L15P_T2_DQ5_34	FMC
FMC	IO_L11N_T1_SRC3_34	Bank 34	U15	IO_B34_LN11	41 42	IO_B34_LN15	U20	Bank 34	IO_L15N_T2_DQ5_34	FMC
FMC	IO_L6P_T0_34	Bank 34	P14	IO_B34_LP6	43 44	IO_B34_LP10	V15	Bank 34	IO_L10P_T1_34	FMC
FMC	IO_L6N_T0_VREF_34	Bank 34	R14	IO_B34_LN6	45 46	IO_B34_LN10	W15	Bank 34	IO_L10N_T1_34	FMC
FMC	IO_L4P_T0_34	Bank 34	V12	IO_B34_LP4	47 48	IO_B34_LP16	V20	Bank 34	IO_L16P_T2_34	FMC
FMC	IO_L4N_T0_34	Bank 34	W13	IO_B34_LN4	49 50	IO_B34_LN16	W20	Bank 34	IO_L16N_T2_34	FMC
				GND	51 52	GND				
FMC	IO_L7P_T1_34	Bank 34	Y16	IO_B34_LP7	53 54	IO_B34_LP17	Y18	Bank 34	IO_L17P_T2_34	FMC
FMC	IO_L7N_T1_34	Bank 34	Y17	IO_B34_LN7	55 56	IO_B34_LN17	Y19	Bank 34	IO_L17N_T2_34	FMC
FMC	IO_L3P_T0_DQ5_PUDC_B_34	Bank 34	U13	IO_B34_LP3	57 58	IO_B34_LP8	W14	Bank 34	IO_L8P_T1_34	FMC
HDMI Interrupt	IO_L3N_T0_DQ5_34	Bank 34	V13	IO_B34_LN3	59 60	IO_B34_LN8	Y14	Bank 34	IO_L8N_T1_34	FMC
FMC	IO_L1P_T0_34	Bank 34	T11	IO_B34_LP1	61 62	IO_B34_LP2	T12	Bank 34	IO_L2P_T0_34	FMC
FMC	IO_L1N_T0_34	Bank 34	T10	IO_B34_LN1	63 64	IO_B34_LN2	U12	Bank 34	IO_L2N_T0_34	FMC
				GND	65 66	GND				
				NC	67 68	NC				
				NC	69 70	NC				
				NC	71 72	NC				
				NC	73 74	NC				
				NC	75 76	NC				
				NC	77 78	NC				
				GND	79 80	GND				